

REMARKS/ARGUMENTS

The Office Action mailed April 16, 2007 has been carefully considered. Reconsideration in view of the following remarks is respectfully requested.

Amendments to Claims

Claims 1, 11, 19 and 27 are amended herein only to correct minor typographical errors. No substantive changes have been made.

The 35 U.S.C. § 102 Rejection

Claims 1, 2, 4, 5, 9, 11, 12, 17, 20, 22, 25, 27 and 30 were rejected under 35 U.S.C. § 102(a) as being allegedly anticipated by Nelson¹. This rejection is respectfully traversed.

Applicants respectfully submit that the invention as claimed, for example in claim 1, is not anticipated by Nelson. Claim 1 includes the following limitations.

A core wireless engine design comprising:

a transceiver;

a microprocessor; and

a standardized interface arrangement, the standardized interface arrangement

adapted to be interconnected to a variety of types of host interfaces implementing a plurality of bus standards, each host interface designed to interface with the standardized interface arrangement.

(Claim 1) (Emphasis added)

¹ U.S. Patent No. 6,404,393

Applicants respectfully submit that Nelson does not disclose a standardized interface arrangement as claimed. The Examiner has cited generally the description of Figure 1 of Nelson as anticipating the claimed invention.

A thorough reading of Nelson makes clear that a standardized interface is not disclosed. Rather Nelson discloses a peripheral component implementing multiple types of interface. Nelson discloses the following.

The present invention provides a peripheral component providing multiple types of interfaces. Embodiments of the present invention provide for a peripheral component with a receptacle interface, a wireline interface, and a wireless interface. Embodiments provide for a such a peripheral component being a type II PCMCIA card. Embodiments provide for such as device which interferes minimally with the logical circuitry inside the peripheral component.

A peripheral component providing multiple types of interfaces is disclosed. In one embodiment, the peripheral component comprises a housing with logical circuitry within. A platform operable to move in and out of the housing is coupled to the housing. The platform has a receptacle interface which is electrically connected to the logical circuitry. Additionally, the platform has a wireline interface also electrically connected to the logical circuitry. Furthermore, an antenna is embedded in the platform and electrically connected to the logical circuitry.

(Nelson, col. 2, lines 8 – 14).

As cited by the Examiner, Nelson further discloses the following.

In the present embodiment, peripheral component 108 is a type II PCMCIA card. The peripheral component 108 has three different types of interfaces on it. For example, the peripheral component 108 has a receptacle interface, which is an X-Jack.TM. in a preferred embodiment. In other embodiments, the receptacle is any receptacle suited to receive an RJ11 plug and/or an RJ-45 plug. The receptacle allows the host computer 120 to connect through a phone line (RJ-11) or Ethernet connection (RJ-45) to, for example, the Internet or a LAN 125. The receptacle may also be suitable for an ISDN connection, a Digital Subscriber Line connection (DSL), or the like.

The peripheral component 108 also comprises a wireline connector. The wireline connector is suitable for any of a number of well known communication standards and protocols, e.g., serial, parallel, SCSI, Firewire (IEEE 1394), etc. In a preferred embodiment, wireline connector is a 15-pin connector. In different embodiments, the wireline connection allows the host computer 120 to interface with a peripheral device 135 via one of the various wireline interfaces, for example, a serial interface, a parallel interface, a SCSI interface, an IEEE 1394 interface, etc.

The peripheral component 108 also has a wireless interface. In one embodiment, this is an

antenna for sending and receiving wireless signals. The antenna couples to a radio frequency (RF) device 200.

(Nelson, col. 3, line 44 – col. 4, line 3)

Nelson further discloses the following.

In accordance with the present invention, peripheral component 108 and host device 120 are coupled via a single bus 100. Peripheral component 108 can be coupled to computer system 120 using any of a variety of physical bus interfaces (e.g., host interface 110, peripheral interface 115), including but not limited to a Universal Serial Bus (USB) interface, Personal Computer (PC) Card interface, CardBus or Peripheral Component Interconnect (PCI) interface, mini-PCI interface, Personal Computer Memory Card International Association (PCMCIA) interface, Industry Standard Architecture (ISA) interface, or RS-232 interface. In the present embodiment, interface 110 runs software (e.g., a virtual device driver) that allows peripheral component 108 to interface with the operating system of the host device (e.g., computer system 120). In a preferred embodiment, the bus 100 is a Personal Computer Memory Card International Association (PCMCIA) interface.

(Nelson, col. 4, lines 23 – 39)

These portions of Nelson disclose only a peripheral component providing multiple types of interfaces and not a standardized interface arrangement that is adapted to be interconnected to a variety of types of host interfaces implementing a plurality of bus standards as claimed.

Applicant respectfully requests the Examiner to indicate specifically where in Nelson a standardized interface arrangement is disclosed. Further, as claimed, the invention includes the limitation that each host interface is designed to interface with the standardized interface arrangement. Such limitation is not found in Nelson.

Given that all pending claims include similar limitations to those found in claim 1, applicant respectfully submits that all pending claims are not anticipated by Nelson.

35 U.S.C. § 103 Rejections

Claims 3, 6-8, 10, 15, 16, 18, 23, 24, 26, 28, 29 and 31 were rejected under 35 U.S.C. § 103(a) as being allegedly unpatentable over Nelson in view of Lazzarotto².

Claims 5, 13 and 14 were rejected under 35 U.S.C. § 103(a) as being allegedly unpatentable over Nelson in view of Pitsoulakis³.

Claim 21 was rejected under 35 U.S.C. § 103(a) as being allegedly unpatentable over Nelson in view of Shiozaki⁴.

These rejections are respectfully traversed.

Applicants respectfully submit that none of the cited references including Nelson, Lazzarotto, Pitsoulakis, and Shiozaki, alone or in any combination one with another render the claimed invention obvious. Applicants respectfully submit that none of Lazzarotto, Pitsoulakis, and Shiozaki, remedy the defects of Nelson as discussed above and as discussed in previous responses.

In view of the foregoing, it is respectfully asserted that the claims are now in condition for allowance.

Request for Entry of Amendment

Entry of this Amendment will place the Application in better condition for allowance, or at the least, narrow any issues for an appeal. Accordingly, entry of this Amendment is appropriate and is respectfully requested.

² U.S. Patent No. 6,782,245

³ U.S. Patent No. 7,092,375

Conclusion

It is believed that this Amendment places the above-identified patent application into condition for allowance. Early favorable consideration of this Amendment is earnestly solicited.

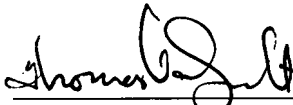
If, in the opinion of the Examiner, an interview would expedite the prosecution of this application, the Examiner is invited to call the undersigned attorney at the number indicated below.

Applicant respectfully requests that a timely Notice of Allowance be issued in this case. Please charge any additional required fee or credit any overpayment not otherwise paid or credited to our deposit account No. 50-1698.

Respectfully submitted,

THELEN REID BROWN
RAYSMAN & STEINER LLP

Dated: July 16, 2007



Thomas Van Zandt
Reg. No. 43,219

THELEN REID BROWN RAYSMAN & STEINER LLP
P.O. Box 640640
San Jose, CA 95164-0640
(408) 292-5800 Telephone
(408) 287-8040 Facsimile